

## OEM-MC Mini

Start Integrating Today!

- ✓ High Performances, High Channel Count
- ✓ Small Form Factor, Easy Mechanical Integration
- ✓ Open Platform, Create Custom Solutions & Products

### PULSER

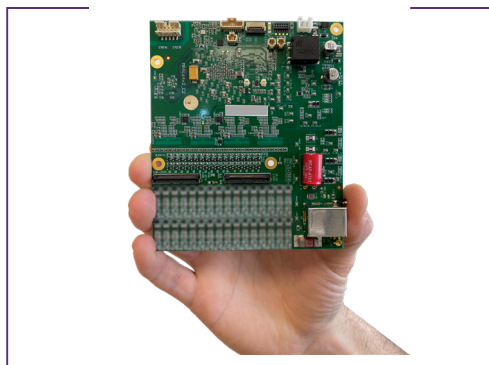
Pulse Voltage	150 V
Pulse Type	Negative Square
Pulse Width	20 – 1000 ns
Pulse Width Resolution	4 ns
Maximum PRF	20 kHz (higher options available)

### RECEIVER

Receiver Resolution	14 bits
Receiver Gain Range	100 dB
Receiver Bandwidth	50 kHz to 20 MHz
Receiver TCG	45 dB

### SIGNAL PROCESSING

FIR Filter	Up to 64 taps
Different Filter per Cycle	Choose from 15 User Defined Filters
Ascan Sampling	100 MHz
Decimation	50, 33, 25, 16.65, 14.28, 12.5 MHz...
Acquire All Ascans	Yes
Ascan Length	16 k Points
Max number of Cycles	4096 Cycles
Gates	4 (Amplitude, TOF)
Gates mode	Any (Peak, Flank, Zero before crossing, Zero after crossing)



Photos and specifications not contractual

### COMMUNICATION

Communication link	LAN (UDP protocol, Gigabit Ethernet)
Usefull UT data flow	100 MB/s <sup>1</sup>

### SYSTEM

Mode	Multiplexed
Configuration	16, 32, 64
Multiplatform Compatibility	With all AOS products
Dimensions (LxWxH)	115x150 mm / 4.53x5.9 in.
Weight	< 250 g / 0.55 lb
Available configurations	Pulse/Echo, Pitch&Catch
Interface integration	Heat plate with 4 screw holes (can be interfaced with a heat sink or cold plate)
Temperature / Humidity Sensors	Yes
Connector board	Available (Application / Customer Defined)
Open Source SDK	Yes (Fully Documented API)
Software languages	C++, C#, LabVIEW, MATLAB, Python and more
Power Consumption	10 W <sup>2</sup>

### I/O MANAGEMENT

Encoders	X, Y
Encoders Modes	Quadrature, Quadrature4edges, Direction Count, Forward, Backward
Synch In	Pulse Trig, Sequence Trig, Encoders
Synch Out	Pulse Trig, Sequence Trig, Output
TimeStamps	Yes (Position and line speed)
Pin Assignments	Programmable
Number I/O	8



<sup>1</sup>The maximum data rate can vary according to the PC, the OS setting, and the Software environment.  
<sup>2</sup>Measured at a 2 kHz PRF with a 5 MHz probe setting, all channels enabled.